

register	bit(s)	
	32	vmm configuration data start
	32	vmm configuration data end
	1	system reset (global reset and config)
	1	data acquisition start
	32	FIFO out
	32	FIFO words
	32	FIFO status and control
	16	clk_dt_period_cnt
	16	clk_dt_dutycycle_cnt
	16	clk_tk_period_cnt
	16	clk_tk_dutycycle_cnt
	16	clk_tp_period_cnt
	16	clk_tp_dutycycle_cnt
	16	clk_bc_period_cnt
	16	clk_bc_dutycycle_cnt
	32	clk_tk_sync_cnt
	12	external trigger bcid offset
	3	vmm to configure
	3	vmm to display (external probes)
	3	external probe set
	8	mmfe8