

Word#	Definition	VME Address	Message Builder Code
---	<i>Begin Transmission</i>		71000000
1	Upper 16 bits of VBD Word Count = 0		70000000
2	VBD Word Count = 0X2B6 or 694 decimal		700002B6

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iogen = **L1caltrackHeader** 6 16-bit Words

3	Word Count ( = 1388 decimal or 0x056C )	CONSTANT	7000056C
4	Module ID	0x1002FFFC	9042FFFC
5	Local (MTCM) Crossing Number (byte 6)	MTCM DPM 0x09 ???	40800032
6	Local (MTCM) Turn Number	MTCM DPM 0x29 ???	40800012
7	Event Status = MTCM Event Status Register 1	MTCM DPM 0x0a = 0x10400004	90400000
8	Event Status = MTCM Event Status Register 2 = Crate	0x1002FFFE	9042FFFE

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iogen = **L1caltrackCMInfo** 12 16-bit Words

9	MTCM Serial Number	CONSTANT	70005555
10	MTCM Latched Error Register	0x10400002	90400002
11	MTCM FPGA Download Control Register	0x10400034	90400034
12	MTCM Trigger Logic FPGA Program ID	0x10000000	90000000
13	MTCM Message Builder FPGA Program ID	0x10010000	90010000
14	MTCM MTCxx Readout Mask	0x1040001E	9040001E
15	MTCM MTCxx Trigger Mask	0x10400020	90400020
16	MTCM L1 Error Mask	0x10400024	90400024
17	MTCM Spare	CONSTANT	7000FFFF
18	MTCM Regional Trigger Decision[11..0]	MTCM DPM 0x08	40800004
19	MTCM Regional Trigger Decision[23..12]	MTCM DPM 0x28	40800024
20	MTCM Regional Trigger Decision[35..24]	MTCM DPM 0x48	40800044

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iogen = **L1caltrackTCErrorBlock** 13 16-bit Words

21	MTCxx 0	CONSTANT	
22	Flavor board status	0x00002C	
23	Latched FIFO status/error	CONSTANT	
24	Crossing #/Error register (32-bit)	DPM 0x060	
25	Error Register	( Low 16 bits )	
26	Latched Error Register	0x000086	
27	Latched Serial Lock A	0x00008A	
28	Latched Serial Lock B	0x0000B2	
29	Latched Input Full A	0x00008E	
30	Latched Input Full B	0x0000B6	
31	Latched Serial Parity A	0x000092	
32	Latched Serial Parity B	0x0000B8	
33	FPGA checksum	CONSTANT	

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34-46	MTCxx #1 Error Block		
47-59	MTCxx #2 Error Block		
60-72	MTCxx #3 Error Block		
73-85	MTCxx #4 Error Block		
86-98	MTCxx #5 Error Block		
99-111	MTCxx #6 Error Block		
112-124	MTCxx #7 Error Block		

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iogen =            **L1caltrackTriggerBlock**            **7 16-bit Words**

125	Octant 0	CONSTANT	
126	Trigger Word 0	DPM 0x030 ( low 16 bits, high = 0 )	
127	Trigger Word 1	DPM 0x070 ( low 16 bits, high = 0 )	
128	Trigger Word 2	DPM 0x0B0 ( low 16 bits, high = 0 )	
129	Trigger Word 3	DPM 0x0F0 ( low 16 bits, high = 0 )	
130	Trigger Word 4	DPM 0x130 ( low 16 bits, high = 0 )	
131	Trigger Word 5	DPM 0x170 ( low 16 bits, high = 0 )	

132-138	MTCxx Octant 1 Trigger Block		
139-145	MTCxx Octant 2 Trigger Block		

146-152	MTCxx Octant 3 Trigger Block		
153-159	MTCxx Octant 4 Trigger Block		
160-166	MTCxx Octant 5 Trigger Block		
167-173	MTCxx Octant 6 Trigger Block		
175-180	MTCxx Octant 7 Trigger Block		

---	<i>End Transmission (if Short Message)</i>		
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**iogen = L1caltrackTCInfoBlock 11 16-bit Words**

181	MTCxx #0 & MTFB Serial Number	0x000000	
182	FPGA 0	0x000002	
183	Serial Input Mask A	0x000006	
184	Serial Input Mask B	0x0000A0	
185	Serial Input Trigger Mask A	0x000008	
186	Serial Input Trigger Mask B	0x0000A2	
187	Lock Error Mask A	0x00000A	
188	Lock Error Mask B	0x0000A4	
189	Parity Error Mask A	0x00000C	
190	Parity Error Mask B	0x0000A6	
191	Multiplexer Control	0x00000E	

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192-202	MTCxx #1 Info Block		
203-213	MTCxx #2 Info Block		
214-224	MTCxx #3 Info Block		
225-235	MTCxx #4 Info Block		
236-246	MTCxx #5 Info Block		
247-257	MTCxx #6 Info Block		
258-268	MTCxx #7 Info Block		

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**iogen = L1caltrackTCInputBlock 140 16-bit Words**

269	MTCxx #0 Input Data Rcvr 0 Strobe 0	DPM 0x000 ( High 16 bits )	
270	MTCxx #0 Input Data Rcvr 1 Strobe 0	( Low 16 bits )	

271	MTCxx #0 Input Data Rcvr 0 Strobe 1	DPM 0x040	
272	MTCxx #0 Input Data Rcvr 1 Strobe 1		
273	MTCxx #0 Input Data Rcvr 0 Strobe 2	DPM 0x080	
274	MTCxx #0 Input Data Rcvr 1 Strobe 2		
275	MTCxx #0 Input Data Rcvr 0 Strobe 3	DPM 0x0C0	
276	MTCxx #0 Input Data Rcvr 1 Strobe 3		
277	MTCxx #0 Input Data Rcvr 0 Strobe 4	DPM 0x100	
278	MTCxx #0 Input Data Rcvr 1 Strobe 4		
279	MTCxx #0 Input Data Rcvr 0 Strobe 5	DPM 0x140	
280	MTCxx #0 Input Data Rcvr 1 Strobe 5		
281	MTCxx #0 Input Data Rcvr 0 Strobe 6	DPM 0x180	
282	MTCxx #0 Input Data Rcvr 1 Strobe 6		
283	MTCxx #0 Input Data Rcvr 2 Strobe 0	DPM 0x004	
284	MTCxx #0 Input Data Rcvr 3 Strobe 0		
285	MTCxx #0 Input Data Rcvr 2 Strobe 1	DPM 0x044	
286	MTCxx #0 Input Data Rcvr 3 Strobe 1		
287	MTCxx #0 Input Data Rcvr 2 Strobe 2	DPM 0x084	
288	MTCxx #0 Input Data Rcvr 3 Strobe 2		
289	MTCxx #0 Input Data Rcvr 2 Strobe 3	DPM 0x0C4	
290	MTCxx #0 Input Data Rcvr 3 Strobe 3		
291	MTCxx #0 Input Data Rcvr 2 Strobe 4	DPM 0x104	
292	MTCxx #0 Input Data Rcvr 3 Strobe 4		
293	MTCxx #0 Input Data Rcvr 2 Strobe 5	DPM 0x144	
294	MTCxx #0 Input Data Rcvr 3 Strobe 5		
295	MTCxx #0 Input Data Rcvr 2 Strobe 6	DPM 0x184	
296	MTCxx #0 Input Data Rcvr 3 Strobe 6		
297	MTCxx #0 Input Data Rcvr 4 Strobe 0	DPM 0x008	
298	MTCxx #0 Input Data Rcvr 5 Strobe 0		
299	MTCxx #0 Input Data Rcvr 4 Strobe 1	DPM 0x048	
300	MTCxx #0 Input Data Rcvr 5 Strobe 1		
301	MTCxx #0 Input Data Rcvr 4 Strobe 2	DPM 0x088	
302	MTCxx #0 Input Data Rcvr 5 Strobe 2		
303	MTCxx #0 Input Data Rcvr 4 Strobe 3	DPM 0x0C8	
304	MTCxx #0 Input Data Rcvr 5 Strobe 3		
305	MTCxx #0 Input Data Rcvr 4 Strobe 4	DPM 0x108	
306	MTCxx #0 Input Data Rcvr 5 Strobe 4		
307	MTCxx #0 Input Data Rcvr 4 Strobe 5	DPM 0x148	

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308	MTCxx #0 Input Data Rcvr 5 Strobe 5		
309	MTCxx #0 Input Data Rcvr 4 Strobe 6	DPM 0x188	
310	MTCxx #0 Input Data Rcvr 5 Strobe 6		
311	MTCxx #0 Input Data Rcvr 6 Strobe 0	DPM 0x00C	
312	MTCxx #0 Input Data Rcvr 7 Strobe 0		
313	MTCxx #0 Input Data Rcvr 6 Strobe 1	DPM 0x04C	
314	MTCxx #0 Input Data Rcvr 7 Strobe 1		
315	MTCxx #0 Input Data Rcvr 6 Strobe 2	DPM 0x08C	
316	MTCxx #0 Input Data Rcvr 7 Strobe 2		
317	MTCxx #0 Input Data Rcvr 6 Strobe 3	DPM 0x0CC	
318	MTCxx #0 Input Data Rcvr 7 Strobe 3		
319	MTCxx #0 Input Data Rcvr 6 Strobe 4	DPM 0x10C	
320	MTCxx #0 Input Data Rcvr 7 Strobe 4		
321	MTCxx #0 Input Data Rcvr 6 Strobe 5	DPM 0x14C	
322	MTCxx #0 Input Data Rcvr 7 Strobe 5		
323	MTCxx #0 Input Data Rcvr 6 Strobe 6	DPM 0x18C	
324	MTCxx #0 Input Data Rcvr 7 Strobe 6		
325	MTCxx #0 Input Data Rcvr 8 Strobe 0	DPM 0x010	
326	MTCxx #0 Input Data Rcvr 9 Strobe 0		
327	MTCxx #0 Input Data Rcvr 8 Strobe 1	DPM 0x050	
328	MTCxx #0 Input Data Rcvr 9 Strobe 1		
329	MTCxx #0 Input Data Rcvr 8 Strobe 2	DPM 0x090	
330	MTCxx #0 Input Data Rcvr 9 Strobe 2		
331	MTCxx #0 Input Data Rcvr 8 Strobe 3	DPM 0x0D0	
332	MTCxx #0 Input Data Rcvr 9 Strobe 3		
333	MTCxx #0 Input Data Rcvr 8 Strobe 4	DPM 0x110	
334	MTCxx #0 Input Data Rcvr 9 Strobe 4		
335	MTCxx #0 Input Data Rcvr 8 Strobe 5	DPM 0x150	
336	MTCxx #0 Input Data Rcvr 9 Strobe 5		
337	MTCxx #0 Input Data Rcvr 8 Strobe 6	DPM 0x190	
338	MTCxx #0 Input Data Rcvr 9 Strobe 6		
339	MTCxx #0 Input Data Rcvr 10 Strobe 0	DPM 0x014	
340	MTCxx #0 Input Data Rcvr 11 Strobe 0		
341	MTCxx #0 Input Data Rcvr 10 Strobe 1	DPM 0x054	
342	MTCxx #0 Input Data Rcvr 11 Strobe 1		
343	MTCxx #0 Input Data Rcvr 10 Strobe 2	DPM 0x094	
344	MTCxx #0 Input Data Rcvr 11 Strobe 2		

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345	MTCxx #0 Input Data Rcvr 10 Strobe 3	DPM 0x0D4	
346	MTCxx #0 Input Data Rcvr 11 Strobe 3		
347	MTCxx #0 Input Data Rcvr 10 Strobe 4	DPM 0x114	
348	MTCxx #0 Input Data Rcvr 11 Strobe 4		
349	MTCxx #0 Input Data Rcvr 10 Strobe 5	DPM 0x154	
350	MTCxx #0 Input Data Rcvr 11 Strobe 5		
351	MTCxx #0 Input Data Rcvr 10 Strobe 6	DPM 0x194	
352	MTCxx #0 Input Data Rcvr 11 Strobe 6		
353	MTCxx #0 Input Data Rcvr 12 Strobe 0	DPM 0x018	
354	MTCxx #0 Input Data Rcvr 13 Strobe 0		
355	MTCxx #0 Input Data Rcvr 12 Strobe 1	DPM 0x058	
356	MTCxx #0 Input Data Rcvr 13 Strobe 1		
357	MTCxx #0 Input Data Rcvr 12 Strobe 2	DPM 0x098	
358	MTCxx #0 Input Data Rcvr 13 Strobe 2		
359	MTCxx #0 Input Data Rcvr 12 Strobe 3	DPM 0x0D8	
360	MTCxx #0 Input Data Rcvr 13 Strobe 3		
361	MTCxx #0 Input Data Rcvr 12 Strobe 4	DPM 0x118	
362	MTCxx #0 Input Data Rcvr 13 Strobe 4		
363	MTCxx #0 Input Data Rcvr 12 Strobe 5	DPM 0x158	
364	MTCxx #0 Input Data Rcvr 13 Strobe 5		
365	MTCxx #0 Input Data Rcvr 12 Strobe 6	DPM 0x198	
366	MTCxx #0 Input Data Rcvr 13 Strobe 6		
367	MTCxx #0 Input Data Rcvr 14 Strobe 0	DPM 0x01C	
368	MTCxx #0 Input Data Rcvr 15 Strobe 0		
369	MTCxx #0 Input Data Rcvr 14 Strobe 1	DPM 0x05C	
370	MTCxx #0 Input Data Rcvr 15 Strobe 1		
371	MTCxx #0 Input Data Rcvr 14 Strobe 2	DPM 0x09C	
372	MTCxx #0 Input Data Rcvr 15 Strobe 2		
373	MTCxx #0 Input Data Rcvr 14 Strobe 3	DPM 0x0DC	
374	MTCxx #0 Input Data Rcvr 15 Strobe 3		
375	MTCxx #0 Input Data Rcvr 14 Strobe 4	DPM 0x11C	
376	MTCxx #0 Input Data Rcvr 15 Strobe 4		
377	MTCxx #0 Input Data Rcvr 14 Strobe 5	DPM 0x15C	
378	MTCxx #0 Input Data Rcvr 15 Strobe 5		
379	MTCxx #0 Input Data Rcvr 14 Strobe 6	DPM 0x19C	
380	MTCxx #0 Input Data Rcvr 15 Strobe 6		
381	MTCxx #0 Input Data Rcvr 16 Strobe 0	DPM 0x028	

382	MTCxx #0 Input Data Rcvr 17 Strobe 0		
383	MTCxx #0 Input Data Rcvr 16 Strobe 1	DPM 0x068	
384	MTCxx #0 Input Data Rcvr 17 Strobe 1		
385	MTCxx #0 Input Data Rcvr 16 Strobe 2	DPM 0x0A8	
386	MTCxx #0 Input Data Rcvr 17 Strobe 2		
387	MTCxx #0 Input Data Rcvr 16 Strobe 3	DPM 0x0E8	
388	MTCxx #0 Input Data Rcvr 17 Strobe 3		
389	MTCxx #0 Input Data Rcvr 16 Strobe 4	DPM 0x128	
390	MTCxx #0 Input Data Rcvr 17 Strobe 4		
391	MTCxx #0 Input Data Rcvr 16 Strobe 5	DPM 0x168	
392	MTCxx #0 Input Data Rcvr 17 Strobe 5		
393	MTCxx #0 Input Data Rcvr 16 Strobe 6	DPM 0x1A8	
394	MTCxx #0 Input Data Rcvr 17 Strobe 6		
395	MTCxx #0 Input Data Rcvr 18 Strobe 0	DPM 0x02C	
396	MTCxx #0 Input Data Rcvr 19 Strobe 0		
397	MTCxx #0 Input Data Rcvr 18 Strobe 1	DPM 0x06C	
398	MTCxx #0 Input Data Rcvr 19 Strobe 1		
399	MTCxx #0 Input Data Rcvr 18 Strobe 2	DPM 0x0AC	
400	MTCxx #0 Input Data Rcvr 19 Strobe 2		
401	MTCxx #0 Input Data Rcvr 18 Strobe 3	DPM 0x0EC	
402	MTCxx #0 Input Data Rcvr 19 Strobe 3		
403	MTCxx #0 Input Data Rcvr 18 Strobe 4	DPM 0x12C	
404	MTCxx #0 Input Data Rcvr 19 Strobe 4		
405	MTCxx #0 Input Data Rcvr 18 Strobe 5	DPM 0x16C	
406	MTCxx #0 Input Data Rcvr 19 Strobe 5		
407	MTCxx #0 Input Data Rcvr 18 Strobe 6	DPM 0x1AC	
408	MTCxx #0 Input Data Rcvr 19 Strobe 6		

409-548	MTCxx #1 Input Data		
549-688	MTCxx #2 Input Data		
689-828	MTCxx #3 Input Data		
829-968	MTCxx #4 Input Data		
969-1108	MTCxx #5 Input Data		
1109-1248	MTCxx #6 Input Data		
1249-1388	MTCxx #7 Input Data		

---	<i>End Transmission (if Long Message)</i>		
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Definition	VME Address	Message Builder Command
<i>Begin Transmission</i>		71000000
Upper 16 bits of VBD Word Count = 0	CONSTANT	70000000
VBD Word Count ( = 70 or 0x46 )	CONSTANT	70000046

### L1caltrackHeader

6 16-bit Words

Word Count (= 140 or 0x8C )	CONSTANT	7000008B
Module ID	0x249	70000249
Local (MTCM) Crossing Number (byte 6)	MTCM DPM 0x32	40800032
Local (MTCM) Turn Number	MTCM DPM 0x12	40800012
Event Status = MTCM Event Status Register 1	MTCM DPM 0x0a ??? = 0x1040000	90400000
Event Status = MTCM Event Status Register 2 = Crate	0x1002FFFE	9042FFFE

### L1caltrackCInfo

12 16-bit Words

MTCM Serial Number	CONSTANT	70005555
MTCM Latched Error Register	0x10400002	90400002
MTCM FPGA Download Control Register	0x10400034	90400034
MTCM Trigger Logic FPGA FPGA Program ID	0x10000000	90000000
MTCM Message Builder FPGA Program ID	0x10010000	90010000
MTCM MTCxx Readout Mask	0x1040001E	9040001E
MTCM MTCxx Trigger Mask	0x10400020	90400020
MTCM L1 Error Mask	0x10400024	90400024
MTCM Spare	CONSTANT	70000011
MTCM Regional Trigger Decision[11..0]	MTCM DPM 0x04	40800004
MTCM Regional Trigger Decision[23..12]	MTCM DPM 0x24	40800024
MTCM Regional Trigger Decision[35..24]	MTCM DPM 0x44	40800044

Read Transfer Pointer and Compare

D4000000

**L1caltrackTCErrorBlock****13 16-bit Words**

MTCxx 4	CONSTANT	70004444
Flavor board status	0x2400002C	B400002C
Latched FIFO status/error	CONSTANT	70000017
Crossing #/Error register (32-bit)	DPM 0x060	54040060
Error Register	( Low 16 bits )	
Latched Error Register	0x24000086	B4000086
Latched Serial Lock A	0x2400008A	B400008A
Latched Serial Lock B	0x240000B2	B40000B2
Latched Input Full A	0x2400008E	B400008E
Latched Input Full B	0x240000B6	B40000B6
Latched Serial Parity A	0x24000092	B4000092
Latched Serial Parity B	0x240000B8	B40000B8
FPGA checksum	CONSTANT	70000021

**CoorTerms[32]****32 16-bit Words**

Coor Terms 0	0x24000032	B4000032
Coor Terms 1	0x24000034	B4000034
Coor Terms 2	0x24000036	B4000036
Coor Terms 3	0x24000038	B4000038
Coor Terms 4	0x2400003A	B400003A
Coor Terms 5	0x2400003C	B400003C
Coor Terms 6	0x2400003E	B400003E
Coor Terms 7	0x24000040	B4000040
Coor Terms 8	0x24000042	B4000042
Coor Terms 9	0x24000044	B4000044
Coor Terms 10	0x24000046	B4000046
Coor Terms 11	0x24000048	B4000048
Coor Terms 12	0x2400004A	B400004A
Coor Terms 13	0x2400004C	B400004C
Coor Terms 14	0x2400004E	B400004E

Coor Terms 15	0x24000050	B4000050
Coor Terms 16	0x24000052	B4000052
Coor Terms 17	0x24000054	B4000054
Coor Terms 18	0x24000056	B4000056
Coor Terms 19	0x24000058	B4000058
Coor Terms 20	0x2400005A	B400005A
Coor Terms 21	0x2400005C	B400005C
Coor Terms 22	0x2400005E	B400005E
Coor Terms 23	0x24000060	B4000060
Coor Terms 24	0x24000062	B4000062
Coor Terms 25	0x24000064	B4000064
Coor Terms 26	0x24000066	B4000066
Coor Terms 27	0x24000068	B4000068
Coor Terms 28	0x2400006A	B400006A
Coor Terms 29	0x2400006C	B400006C
Coor Terms 30	0x2400006E	B400006E
Coor Terms 31	0x24000070	B4000070

### L1caltrackTriggerBlock

7 16-bit Words

Octant 4	CONSTANT	70004444
Trigger Word 0	DPM 0x030 ( low 16 bits, high = 0 )	54040030
Trigger Word 1	DPM 0x070 ( low 16 bits, high = 0 )	54040070
Trigger Word 2	DPM 0x0B0 ( low 16 bits, high = 0 )	540400B0
Trigger Word 3	DPM 0x0F0 ( low 16 bits, high = 0 )	540400F0
Trigger Word 4	DPM 0x130 ( low 16 bits, high = 0 )	54040130
Trigger Word 5	DPM 0x170 ( low 16 bits, high = 0 )	54040170

### L1caltrackTCInfoBlock

11 16-bit Words

MTCxx #0 & MTFB Serial Number	0x24000000	B4000000
FPGA	0x24000002	B4000002
Serial Input Mask A	0x24000006	B4000006
Serial Input Mask B	0x240000A0	B40000A0

Serial Input Trigger Mask A	0x24000008	B4000008
Serial Input Trigger Mask B	0x240000A2	B40000A2
Lock Error Mask A	0x2400000A	B400000A
Lock Error Mask B	0x240000A4	B40000A4
Parity Error Mask A	0x2400000C	B400000C
Parity Error Mask B	0x240000A6	B40000A6
Multiplexer Control	0x2400000E	B400000E

### L1caltrackReceiverPair

56 16-bit Words

MTM Input Data Rcvr 0 Strobe 0	DPM 0x000 ( High 16 bits )	54040000
MTM Input Data Rcvr 1 Strobe 0	( Low 16 bits )	
MTM Input Data Rcvr 0 Strobe 1	DPM 0x040	54040040
MTM Input Data Rcvr 1 Strobe 1		
MTM Input Data Rcvr 0 Strobe 2	DPM 0x080	54040080
MTM Input Data Rcvr 1 Strobe 2		
MTM Input Data Rcvr 0 Strobe 3	DPM 0x0C0	540400C0
MTM Input Data Rcvr 1 Strobe 3		
MTM Input Data Rcvr 0 Strobe 4	DPM 0x100	54040100
MTM Input Data Rcvr 1 Strobe 4		
MTM Input Data Rcvr 0 Strobe 5	DPM 0x140	54040140
MTM Input Data Rcvr 1 Strobe 5		
MTM Input Data Rcvr 0 Strobe 6	DPM 0x180	54040180
MTM Input Data Rcvr 1 Strobe 6		
MTM Input Data Rcvr 2 Strobe 0	DPM 0x004	54040004
MTM Input Data Rcvr 3 Strobe 0		
MTM Input Data Rcvr 2 Strobe 1	DPM 0x044	54040044
MTM Input Data Rcvr 3 Strobe 1		
MTM Input Data Rcvr 2 Strobe 2	DPM 0x084	54040084
MTM Input Data Rcvr 3 Strobe 2		
MTM Input Data Rcvr 2 Strobe 3	DPM 0x0C4	540400C4
MTM Input Data Rcvr 3 Strobe 3		
MTM Input Data Rcvr 2 Strobe 4	DPM 0x104	54040104
MTM Input Data Rcvr 3 Strobe 4		
MTM Input Data Rcvr 2 Strobe 5	DPM 0x144	54040144
MTM Input Data Rcvr 3 Strobe 5		

MTM Input Data Rcvr 2 Strobe 6	DPM 0x184	54040184
MTM Input Data Rcvr 3 Strobe 6		
MTM Input Data Rcvr 4 Strobe 0	DPM 0x008	54040008
MTM Input Data Rcvr 5 Strobe 0		
MTM Input Data Rcvr 4 Strobe 1	DPM 0x048	54040048
MTM Input Data Rcvr 5 Strobe 1		
MTM Input Data Rcvr 4 Strobe 2	DPM 0x088	54040088
MTM Input Data Rcvr 5 Strobe 2		
MTM Input Data Rcvr 4 Strobe 3	DPM 0x0C8	540400C8
MTM Input Data Rcvr 5 Strobe 3		
MTM Input Data Rcvr 4 Strobe 4	DPM 0x108	54040108
MTM Input Data Rcvr 5 Strobe 4		
MTM Input Data Rcvr 4 Strobe 5	DPM 0x148	54040148
MTM Input Data Rcvr 5 Strobe 5		
MTM Input Data Rcvr 4 Strobe 6	DPM 0x188	54040188
MTM Input Data Rcvr 5 Strobe 6		
MTM Input Data Rcvr 6 Strobe 0	DPM 0x00C	5404000C
MTM Input Data Rcvr 7 Strobe 0		
MTM Input Data Rcvr 6 Strobe 1	DPM 0x04C	5404004C
MTM Input Data Rcvr 7 Strobe 1		
MTM Input Data Rcvr 6 Strobe 2	DPM 0x08C	5404008C
MTM Input Data Rcvr 7 Strobe 2		
MTM Input Data Rcvr 6 Strobe 3	DPM 0x0CC	540400CC
MTM Input Data Rcvr 7 Strobe 3		
MTM Input Data Rcvr 6 Strobe 4	DPM 0x10C	5404010C
MTM Input Data Rcvr 7 Strobe 4		
MTM Input Data Rcvr 6 Strobe 5	DPM 0x14C	5404014C
MTM Input Data Rcvr 7 Strobe 5		
MTM Input Data Rcvr 6 Strobe 6	DPM 0x18C	5404018C
MTM Input Data Rcvr 7 Strobe 6		

Word 8C ( VBD Pad Word )	CONSTANT	7000008C
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<i>End Transmission</i>		71000008
<i>Move Transfer Pointer to Empty Buffer List</i>		E4000000
<i>End Message</i>		F0000000